TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WG14FC

Triple Schmitt Inverter

Features

High output current : ±8 mA (min) at V_{CC} = 3 V

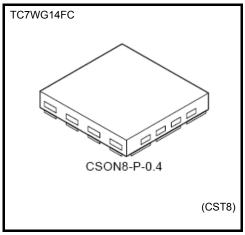
• Super high speed operation: t_{pd} = 4.0 ns (typ.)

at $V_{CC} = 3.3 \text{ V}, 15 \text{pF}$

• Operating voltage range : V_{CC} = 0.9 to 3.6 V

• 5.5-V tolerant inputs

• 3.6-V power down protection outputs



Weight: 0.002 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	-0.5 to 4.6	V
DC input voltage	V _{IN}	-0.5 to 7.0	V
DC output voltage	Vour	-0.5 to 4.6 (Note1)	V
	V _{OUT}	-0.5 to V _{CC} +0.5 (Note2)	V
Input diode current	I _{IK}	-20	mA
Output diode current	lok	-20 (Note3)	mA
DC output current	lout	±25	mA
DC V _{CC} /GND current	Icc	±50	mA
Power dissipation	PD	150 (Note4)	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the

Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

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Note 1: $V_{CC} = 0 V$

Note 2: High or Low State.

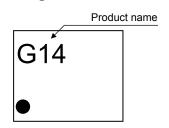
Do not exceed I_{OUT} of absolute maximum ratings.

Note 3: Vout < GND

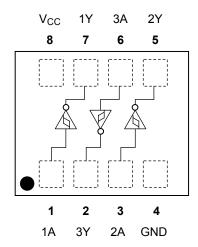
Note 4: Mounted on an FR4 board.

 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ t}, \text{ Cu Pad: } 11.56 \text{ mm}^2)$

Marking



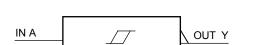
Pin Assignment (top view)



Start of commercial production 2006-04



IEC Logic Symbol



Truth Table

Α	Y
L	Н
Н	L

Operating Ranges

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	0.9 to 3.6	V	
Input voltage	V _{IN}	0 to 5.5	V	
Output voltage	V	0 to 3.6 (Note 5)	V	
	V _{OUT}	0 to V _{CC} (Note 6)		
Output current	I _{OH} /I _{OL}	± 8.0 (Note 7)		
		± 4.0 (Note 8)		
		± 3.0 (Note 9)	mA	
		± 1.7 (Note 10)	MA	
		± 0.3 (Note 11)		
		± 0.02 (Note 12)		
Operating temperature	T _{opr}	-40 to 85	°C	

Note 5: $V_{CC} = 0V$

Note 6: High or Low state.

Note 7: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 8: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

Note 9: $V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$

Note 10: $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$

Note 11: $V_{CC} = 1.1 \text{ to } 1.3 \text{ V}$

Note 12: $V_{CC} = 0.9 V$

2



Electrical Characteristics

DC Characteristics

Characteristics Symbol		Symbol Test Condition				Ta = 25°C			Ta = -40 to 85°C		Unit
		rest condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic	
					0.9	_	_	0.73	_	0.80	
Positive threshold voltage					1.1	_	_	0.86	_	0.93	
		.,			1.4	_	_	1.07	_	1.12	
	V _P	_		1.65	_	_	1.23	_	1.25		
				2.3	_	_	1.66	_	1.68		
Threshold					3.0	_	_	2.14	_	2.15	
Voltage			_		0.9	0.18	_	_	0.07	_	V
					1.1	0.26		_	0.18	_	
	Negative				1.4	0.36	_	_	0.31	_	
	threshold voltage	V _N			1.65	0.45	_	_	0.41	_	
					2.3	0.69		_	0.64	_	
					3.0	0.96	_	_	0.91	_	
					0.9	0.20	_	0.38	0.15	0.53	
					1.1	0.25	_	0.41	0.21	0.53	
Hystorosia Va	Itaga	V			1.4	0.35	_	0.48	0.34	0.57	1 ,
Hysteresis Voltage	VH		_		0.42	_	0.56	0.40	0.60	V	
					2.3	0.60	_	0.74	0.61	0.76	
					3.0	0.79	_	0.93	0.80	0.94	
				I _{OH} =-0.02 mA	0.9	0.75	_	_	0.75	_	V
		Vон	$V_{IN} = V_{IL} \label{eq:VIN}$	$I_{OH} = -0.3 \text{ mA}$	1.1 to 1.3	V _{CC} × 0.75		_	V _{CC} × 0.75	_	
Output voltage	High level			$I_{OH} = -1.7 \text{ mA}$	1.4 to 1.6	V _{CC} × 0.75			V _{CC} × 0.75		
				$I_{OH} = -3.0 \text{ mA}$	1.65 to 1.95	V _{CC} -0.45		_	V _{CC} -0.45	_	
				$I_{OH} = -4.0 \text{ mA}$	2.3 to 2.7	2.0	_	_	2.0	_	
				$I_{OH} = -8.0 \text{ mA}$	3.0 to 3.6	2.48	_	_	2.48	_	
				$I_{OL} = 0.02 \text{ mA}$	0.9	_	_	0.1	_	0.1	
				I _{OL} = 0.3 mA	1.1 to 1.3	_		V _{CC} × 0.25	_	V _{CC} × 0.25	
	Low level	V _{OL}		I _{OL} = 1.7 mA	1.4 to 1.6			V _{CC} × 0.25	_	V _{CC} × 0.25	
				$I_{OL} = 3.0 \text{ mA}$	1.65 to 1.95	-		0.45	_	0.45	
				$I_{OL} = 4.0 \text{ mA}$	2.3 to 2.7			0.4	_	0.4	
				$I_{OL} = 8.0 \text{ mA}$	3.0 to 3.6	-		0.4	_	0.4	
Input leakage	put leakage current I _{IN} V _{IN} = 0 to 5.5 V		0 to 3.6	_		±0.1	_	±1.0	μА		
Power off leak	age current	loff		V _{IN} = 0 to 5.5 V or V _{OUT} =0 to 3.6 V				1.0	_	10.0	μА
Quiescent supply current		I _{CC}	V _{IN} = V _{CC}	$V_{IN} = V_{CC}$ or GND		_	_	1.0	_	10.0	μА

3 2014-03-01

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40 to 85°C		Unit	
			V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
		C _L = 10 pF,	0.9		41.3	_	_		
			1.1 to 1.3	_	18.0	25.4	1.0	40.8	
			1.4 to 1.6		9.5	12.2	1.0	13.5	
		$R_L = 1 M\Omega$	1.65 to 1.95		7.0	8.7	1.0	9.3	
			2.3 to 2.7		4.7	5.7	1.0	6.2	
			3.0 to 3.6		3.7	4.5	1.0	4.7	
		$C_L = 15 pF$, $R_L = 1 M\Omega$	0.9		44.4	_	_		
	^t pLH ^t pHL		1.1 to 1.3		19.3	27.7	1.0	46.9	ns
Propagation delay time			1.4 to 1.6		10.2	13.1	1.0	14.7	
			1.65 to 1.95		7.5	9.3	1.0	9.9	
			2.3 to 2.7		5.0	5.9	1.0	6.4	
			3.0 to 3.6		4.0	4.8	1.0	5.2	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		55.8	_	_	_	
			1.1 to 1.3		24.7	36.3	1.0	59.6	
			1.4 to 1.6		12.9	16.8	1.0	19.2	
			1.65 to 1.95		9.2	11.5	1.0	12.9	
			2.3 to 2.7		5.9	7.1	1.0	8.3	
			3.0 to 3.6		4.9	5.7	1.0	6.6	
Input capacitance	C _{IN}	_	3.6		3	_	_		pF
Power dissipation capacitance	C _{PD}	(Note 13)	0.9 to 3.6	-	11	_	_		pF

Note 13: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

4

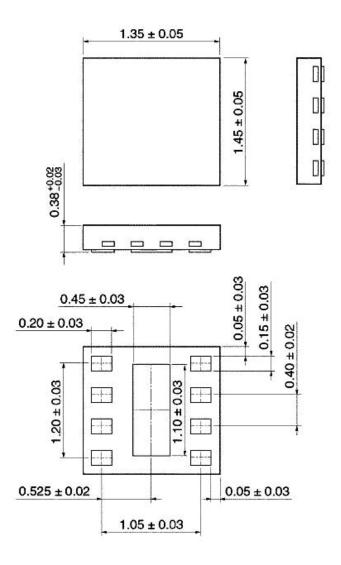
Average operating current can be obtained by the equation:

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$



Package Dimensions

CSON8-P-0.4 Unit: mm



Weight: 0.002 g (typ.)

5 2014-03-01

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